

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: *** Fernando Gonzalez et al. Serial No.: 10/751,141 Filed: December 31, 2003

Transistor Having Vertical Junction For: Edge and Method of Manufacturing

the Same

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313

Group Art Unit: 2815 Nguyen, Joseph H. Examiner:

Atty. Docket: MICS:0114 02-1010

CERTIFICATE OF TRANSMISSION OR MAILING 37 C.F.R. 1.8

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Date

RESPONSE TO FINAL OFFICE ACTION

In response to the Final Official Action mailed on July 14, 2005, Applicants respectfully request reconsideration of the above-identified application in view of the remarks set forth below.